RGUIIIT-Basar/Placement/Notice Board/107/2017   **Date: 24.10.16**

SCHEDULE OF EFFTRONICS FINAL TECHNICAL ROUND

Process: Final technical round+HR+Psychometric round+Fitness test.

1) **26-Sep-16:**   Embedded Engineer (B.Tech ECE) (All levels will be completed in the same day).

2) **27-Sep-16:**   Software Engineer   (B.Tech CSE) (All levels will be completed in the same day).

Reporting time: 07:30 AM
Address: Efftronics systems pvt ltd,Back side of chandana grand,
Brundavan colony,Vijayawada.

LIST OF STUDENTS SHORTLISTED FOR INTERVIEWS OF FIRST TECHNICAL ROUND

S.NO	ID NO	NAME OF STUDENT	BRANCH
1	B111442	ADIKEWAR ARUNA	CSE
2	B111528	KALUVACHARLA PRIYANKA	CSE
3	B111283	VUDUDALA KARTHIK	CSE
4	B111289	KALALI SAIKUMARGOUD	CSE
5	B111176	PABBU UMA	CSE
6	B111131	Akhil Kumar Poola	CSE
7	B111680	GUNDELLI SRIKANTH	ECE
8	B111053	DARAPUREDDY NARENDRA	ECE
9	B111611	GUGULOTH RAJASHEKHAR	ECE
10	B111431	ADAKATLA SAIBABU	ECE
11	B111797	NUTHALAPATI ADINARAYANA	ECE
12	B111799	ARRA RAGHAVENDAR	ECE
13	B111709	KAMA MANEESHA	ECE
14	B111814	ELLENDULA VENKATESH	ECE
15	B111574	THOUTAM VAMSHIKRISHNA	ECE
16	B111102	TAKKELLA YAMINI	ECE
17	B111305	KOMERAGARI KUMAR	ECE

Sd/-

Placement Officer