RGUIIIT-Basar/Placement/Notice Board/031/2018   **Date: 07.09.2017**

JOB DESCRIPTION OF EFFTRONICS

Tentative schedule of drive: 14th Sept 2017 & 16th Sept 2017

Eligibility Criteria:

X,PUC,B.TECH (CGPA)	**70%,70%,70%**
BRANCHES	**ECE & CSE (B. Tech & M. Tech)**
JOD DESIGNATION	**FIRMWARE & SOFTWARE TRAINEE**
CTC	**3.5 LPA**
BOND	**2.5 YRS**
TRAINING PERIOD	**6 MONTHS**
JOINING LOCATION	**VIJAYAWADA**
INTERNSHIP CUM JOB OFFER	**YES**

JOB DESCRIPTION:

- ➢ **Firmware trainee**: To design, develop, prototype, test and troubleshoot analog and digital systems.
- ➢ **Software Trainee**: To design, develop, prototype, test and troubleshoot application software.

INTERNSHIP DETAILS:

Profile Offered	Project Trainee
Job Description	Firmware: To test and troubleshoot analog and digital systems. Software : To test and troubleshoot application software
Stipend	6000/-
Duration (weeks)	24
Medical exclusions, if any	yes

SELECTION PROCESS:

- Written Test
- Personal Interview
- Technical Interview

Registration starts from: 7th Sept 2017, 2:00PM

Registration ends on: 9th Sept 2017, 2:30PM

Registration link: http://efftronics.runexam.com/register.aspx

Sd/-

Placement Office