



Registration for CEREMORPHIC Recruitment Drive

We are delighted to announce the commencement of Ceremorphic Campus Recruitment Drive to recruit fresh Graduates from the following disciplines:

Eligible Disciplines: B.Tech – CSE & ECE.

Digital Design: Key Requirements

- Knowledge in architecture, microarchitecture, and logic design.
- Expertise in AHB/AXI based SoC Architectures.
- Strong knowledge in ARM M-series or A-Series MCU, RISC-V architectures and sub-system integration.
- Knowledge in designing high-speed interfaces like USB, PCIe 4x/5x, Ethernet, Mobile DDR, Quad/Octa-SPI.
- Strong knowledge in peripheral interfaces like SDIO, UART, I2S, I2C, PWM, CAN, etc.
- Strong knowledge in signoff flows including lint, CDC, Synthesis, LEC and STA timing closure.
- Knowledge in low power design methodology (static/dynamic clock gating, power gating, dynamic voltage and frequency scaling).

Software: Key Requirements:

Familiarity with Network Protocols, Python, TensorFlow, PyTorch, Compiler design, Embedded Software : C, C++, programming on GPUs, cloud platforms, Chip-level firmware, Bootloaders, GCC toolchain/compilers and Linux internal

Package: 13.5 LPA per Annum

Group Medical Insurance : 6 Lakh

Stock options: 500

As Ceremorphic will incur cost and time on the training, Employees are required to **serve a for period of 3 years starting from their date of joining**. In case if, decided to leave Ceremorphic during above mentioned period, **candidates are liable to pay INR 4,00,000/- (Rupees Four Lakhs only)** to Ceremorphic.

Your timely registration is essential to confirm your participation and ensure that your details are accurately submitted to the company.

How to Apply:

If meet the eligibility criteria and interested about this opportunity, please apply through the link: [Apply Here](#) by **December 22, 2024, 12:30 PM**.

Upload Resume: <https://forms.gle/HxSx5bKuFmAe9Mg3A>

Sd/-

T & P Office