



Rajiv Gandhi University of Knowledge Technologies

(Telangana Government Act 8 of 2016)

Basar, Nirmal, Telangana State – 504107, India.

A.Y 23-24_E1_SEM2_ELECTRICAL ENGINEERING TIMETABLE

Class	Time	Mon	Tue	Wed	Thu	Fri	Sat		Class	Time	Mon	Tue	Wed	Thu	Fri	Sat
	Slot-1	F	Z	B	D	D	B			Slot-1	A	B	F	Z	F	F
	Slot-2	D	B ₁	C	A	A				Slot-2	C	D	D		C	B
C1 (H&S-001)	Slot-3	A	Z	E	C	C	Y _{B1 B2}		C2 (H&S-002)	Slot-3	D	A	A	W _{B1 B2}	A	B
	Slot-4	B	Z _{B2}	A	B	E				Slot-4		C	E		B	E
	LUNCH BREAK									LUNCH BREAK						
	Slot-5	X _{B1 B2}	W _{B1 B2}	F	F					Slot-5		X _{B1 B2}	Y _{B1 B2}		Z	
	Slot-6			Z						Slot-6					Z _{B1}	
	Slot-7									Slot-7					Z _{B2}	

Class	Time	Mon	Tue	Wed	Thu	Fri	Sat	Almanac			
	Slot-1		D	D	F	E		Commencement of semester-I class work: 01.03.2024			
	Slot-2	A	A	A	F	D	A	Last day of instruction for semester-I: 20.07.2024			
C3 H&S-003	Slot-3	C	C	C	B	B	F	MT's exam date		Assignments submission date	
	Slot-4	E	B	B		Z					
	LUNCH BREAK							MT-1	08-04-2024 10-04-2024	A-1	06-04-2024
	Slot-5	Y _{B1 B2}		X _{B1 B2}	Z _{B1}	W _{B1 B2}		MT-2	10-06-2024 11-06-2024	A-2	07-06-2024
	Slot-6							MT-3	10-07-2024 11-07-2024	A-3	06-07-2024
	Slot-7				Z _{B2}						

S.No	Course code	Course Title	L-T-P	Contact Hours	Faculty
A	EE1201	Network Theory-2	3-0-0	4	Mr. V. Vinay Kumar (C1,C2 & C3)
B	MA1202	Differential Equations & Laplace Transforms	3-1-0	4	Mr. Balraj (C1,C2 & C3)
C	CY1201	Engineering Chemistry	3-0-0	3	Dr. K. Pavani (C1,C2 & C3)
D	CS1202	Programming for Problem Solving	3-0-0	3	Mrs. G.Srujana (C1,C2 & C3)
E	BM1205	Constitution of India	2-0-0	2	Mr. Swapnil (C1,C2 & C3)
F	EC1203	Analog Electronic Circuits-1	3-0-0	3	Guest Faculty (C1,C2 & C3)
W	EE1801	Network Theory Lab	0-0-3	3	Mr. V. Vinay Kumar (C1-B1) Mr.M.Praveen kumar (C2-B1 & C3-B1) Mr.T.Naveen kumar (C1-B2,C2-B2 & C3-B2)
X	CY1801	Engineering Chemistry Lab	0-0-3	3	Dr. K. Pavani (C1, & C2) Dr. J. Kashanna (C3)

Y	CS1802	Programming for Problem Solving Lab	0-0-3	3	Mrs. G.Srujana(C1-B1)&Mrs. J. Bhanupriya C1B2) Mr. B.Srikanth(C2 -B1) & Mrs. JyothiC2-B2) Mr. Revya Naik (C3-B1)&Mrs. Sarika P (C3-B2)
Z	ME1802	Engineering workshop	0-1-2	3	Mrs. Ashwini (C1-B1&B2 & C2- B1&B2) Mr. K. Mahesh (C3- B1&B2)

A.Y 23-24_E1_SEM2_ELECTRICAL ENGINEERING TIMETABLE

SLOTS	TIME
Slot-1	09:00 AM to 09:55 AM
Slot-2	10:00 AM to 10:55 AM
Slot-3	11:00 AM to 11:55 AM
Slot-4	12:00 PM to 12:55 PM
LUNCH BREAK	
Slot-5	2.00 PM to 2:55 PM
Slot-6	3.00 PM to 3:55 PM
Slot-7	4.00 PM to 4:55 PM

(Mr.T.Naveen kumar)
Faculty I/c,
Dept. Timetables

(Mr.V.Vinay Kumar)
HoD/EE

(Mr.K.Mahesh Kumar)
Associate Dean/Engineering