



Rajiv Gandhi University of Knowledge Technologies

(Telangana Government Act 8 of 2016)

Basar, Nirmal, Telangana State – 504107, India.

A.Y 26-27_E1_SEM1_MECHANICAL ENGINEERING TIMETABLE

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat	Year	Time	Mon	Tue	Wed	Thu	Fri	Sat	
C1	Slot-1	B	X	C	C	A		C2	Slot-1	C	B	D	A	X		
	Slot-2	A		B	B	F(B			2)	Slot-2	D	A	F(B2)			F(B1)
	Slot-3	C		A	D					Slot-3	ED	C				
	LUNCH BREAK								LUNCH BREAK							
	Slot-4	Y(B1)	B	Z	A	D			Slot-4	A	Z	B	Y(B1)	A		
	Slot-5				E	Z				Slot-5		B		C		B
	Slot-6	Y(B2)	F(B1)		G	E				Slot-6		E	G			E
	Slot-7									Slot-7				Y(B2)		

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat			Time
C3	Slot-1	A	C	B	D				Slot-1	9:00 AM to 9:55 AM
	Slot-2	B	B	C	A	A			Slot-2	10:00 AM to 10:55 AM
	Slot-3	E	A	D	B	Z			Slot-3	11:00 AM to 11:55 AM
	LUNCH BREAK								LUNCH BREAK	
	Slot-4	C	Y(B1)	F(B2)	Z	G			Slot-4	1.00 PM to 1:55 PM
	Slot-5	X							Slot-5	2.00 PM to 2:55 PM
	Slot-6					E	F1		Slot-6	3.00 PM to 3:55 PM
	Slot-7			Y(B2)					Slot-7	4:00 PM to 4:55 PM

	Course Code	Course Name	L-T-P	Total Contact hours	Faculty
A	MA1105	Matrix Theory and Calculus	3-1-0	4	Mr. Raja Reddy (C1,C2,C3)
B	PH1102	Applied Physics	3-1-0	4	Dr. P Rajendhar(C1,C2), Mr.Naresh Chintha (C3)
C	EE1102	Basic Electrical and Electronics Engineering	3-0-0	3	Mr.T.Ranjith kumar (C1,C2,C3)
D	HS1101	English	2-0-0	2	Mr.D.Vasanth Babu (C1,C2,C3)
X	PH1702	Applied Physics Lab	0-0-3	1.5	Dr.P.Rajendhar(C1-B1,C2-B1), Mr.Naresh Chintha(C3-B1), Mrs.Sujatha (C1-B2,C2-B2), Mr.Laxminarsaiah (C3-B2)
Y	EE1701	Basic Electrical and Electronics Engineering Lab	0-0-2	1	Mr.T.Ranjith kumar (C1,C2,C3)
G	ME1701	Design Thinking	0-0-2	1	Mr.S Siddhaiah(C1), Mr.Sanakumar(C2), Dr.Dilbahar Ali(C3)
Z	ME1702	Engineering Drawing and Computer Graphics	0-1-4	3	Dr,M.Srivastava(C1,C2,C3), Mrs M Ashwini(C3 Theory)
E	BS1101	Environmental Science	0-0-2	0	Dr. Narendar Naik (C1,C2,C3)
F	HS1701	English Language Lab	0-0-2	1	Mr.D.Vasanth Babu (C1,C2,C3)

Mr. S SIDDHAIAH
Time TableI/c

Mr. A CHARAN REDDY
HOD/ME

Dr.K. MAHESH
Associate Dean Engineering