



Innosilica Pan India Hiring Drive – Registration & Online Test Notification

Dear Students,
Greetings from the Training & Placement Cell.

We are pleased to inform you that **Innosilica** is conducting a **Pan India Hiring Drive** for **Fresher to Experienced Engineers** across VLSI and Embedded domains. This is an excellent opportunity for eligible students to begin their careers in the semiconductor industry.

Eligibility:

- B.E / B.Tech / M.E / M.Tech (EC / EEE / E&TC / CS related branches)
- Minimum CGPA: **6.0**

Compensation:

- Bachelors: **4.6 LPA**
- Masters: **5.2 LP**

Skills Preferred:

- Digital Logic Design
- C / C++ / Verilog / VHDL
- Aptitude & Reasoning
- Communication & Problem-Solving Skills

Selection Process:

1. Online Written Test (Aptitude + Technical Basics)
2. Technical Interview
3. HR Interview

Registration Link:

- ☐ <https://forms.gle/Npze5KKaynjccouf8>

☐ Online Test Details

- **Date:** 21 March
- **Time:** 11:00 AM to 11:30 AM (Strict)
- **Exam Link:** <https://forms.gle/TN3218d2arKtiMF59>

☐☐ Important Instructions:

- The test link will open at **11:00 AM** and close at **11:30 AM sharp**
- No late submissions will be accepted
- Use a **Laptop/PC** (recommended)
- Ensure **stable internet & battery backup**
- Keep **notebook, pen, calculator** ready
- **Resume upload is mandatory** (Max size: 1 MB)
- Once submitted, responses **cannot be edited**

Students are advised to **register immediately** and utilize this opportunity.

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Training & Placement Cell

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