



Rajiv Gandhi University of Knowledge Technologies

(Telangana Government Act 8 of 2016)

Basar, Nirmal, Telangana State – 504107, India.

A.Y 24-25_E3_SEM1 ELECTRONICS AND COMMUNICATION ENGINEERING TIME TABLE

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat	Year	Time	Mon	Tue	Wed	Thu	Fri	Sat
C1	Slot-1	A	G	H	E	G	X(B)	C2	Slot-1		E	G	C	A	
	Slot-2	B	E	E	C	A			Slot-2	A	B	E	E	E	
	Slot-3	F	B	A		E			Slot-3	B	A	B	A	C	
	Slot-4	C	F	B		H			Slot-4	F	C	F	H	F	
	LUNCH BREAK								LUNCH BREAK						
	Slot-5		A	C	X(A)	Y			Slot-5	H	Y	X(A)	H	X(B)	
	Slot-6		H	F			(A&B)		Slot-6	G	(A&B)				
Slot-7							Slot-7								

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat	Year	Time	Mon	Tue	Wed	Thu	Fri	Sat
C3	Slot-1	E	A	E	A	G		C4	Slot-1	G	E		E	C	
	Slot-2	F	E	F	E	C			Slot-2	E	A	E	A	H	
	Slot-3	A	C	H	B	H			Slot-3	C	F	F	H	A	X(B)
	Slot-4		B	A					Slot-4		G	H	B		
	LUNCH BREAK								LUNCH BREAK						
	Slot-5	X(A)	H	Y	C	B	X(B)		Slot-5	B	X(A)	C	Y	F	
	Slot-6		G	(A&B)		F			Slot-6			A	(A&B)	B	
Slot-7							Slot-7								

	Time
Slot-1	09:00 AM to 09:55 AM
Slot-2	10:00 AM to 10:55 AM
Slot-3	11:00 AM to 11:55 AM
Slot-4	12:00 PM to 12:55 PM
LUNCH BREAK	
Slot-5	2.00 PM to 2:55 PM
Slot-6	3.00 PM to 3:55 PM
Slot-7	4.00 PM to 4:55 PM

	Course code	Course Name	L-T-P	TOTAL CONTACT HOURS	FACULTY
A	EC3101	Analog and Digital Communications	3-1-0	4	Mr.K.Anjani Kumar(C1,C2,C3,C4)
X	EC3701	Analog and Digital Communications lab	0-0-2	2	Mr.G.Srinivs Sagar (C1-A,C2-A,C3-A,C4-A,C1-B),Mr.K .Karthik(C2-B,C3-B,C4-B)
B	EC3102	Computer Architecture	3-0-0	3	Mrs.B.Neelima (C1,C2),Guest Faculty-2(C3,C4)
C	EC3103	Digital Signal Processing	3-0-0	3	Mrs.Gayathri(C1,C2,C3,C4,C5)
Y	EC3702	Digital Signal Processing lab	0-0-2	2	Mr.A.Dheeraj (C1-A,C2-A,C3-A,C4-A)Mr.A.Sainath Chaitanya(C2-B,C3-B,C4-B) Mrs.Gayathri(C1-B),
E	EC3104	VLSI Engineering	3-1-0	4	Mr.A.Dheeraj(C1,C2), Mr.R.Saidulu(C3,C4)
F	BM3107	Managerial Economics and Financial analysis	3-0-0	3	Mr.J.Swapnil(C1,C2,C3,C4)
G	HS31XX	Corporate Communications	0-0-2	2	Mr.U.Prabhakar,(C1)Mr.K.Venkateswarlu(C2), Mr.D.Vasanth Babu(C3),Mr.K.Krishna Prasad(C4)
H	-----	Data Structures	3-0-0	3	Guest Faculty(C1,C2,C3,C4)

(Mr.R.Saidulu)
Faculty I/c, Dept. Timetables

(Mr.G.Sai Radha Krishna)
(HOD/ECE)

(Mr.K.Mahesh)
Associate Dean Engineering