

Rajiv Gandhi University of Knowledge Technologies

(Telangana Government Act 8 of 2016) Basar, Nirmal, Telangana State – 504107, India.

A.Y 22-23_E1_SEM1_ELECTRONICS AND COMMUNICATION ENGINEERING TIME TABLE

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat	
	Slot-1	Е		С	D			
	Slot-2	A	X	D	Е			
C1	Slot-3	С			A	Z		
(CSE-124)	Slot-4	В		Е	В		W	
		LUNCH BREAK						
	Slot-5		D			В		
	Slot-6	Y	A			A		
	Slot-7							
	Slot-8							

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat
	Slot-1				Е	Е	С
	Slot-2	X	В	Е			D
C2	Slot-3		С	A	В	В	
(ECE-F25)	Slot-4	A	A	D	A		W
	LUNCH BREAK						
	Slot-5	D					
	Slot-6		Y				
	Slot-7				$\mid Z \mid$		
	Slot-8						

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat
	Slot-1		Е		D	D	
C3	Slot-2	Е	A	С		A	
(ECE-G13)	Slot-3	В	D	В	X	Е	
	Slot-4	С		A		В	W
	LUNCH BREAK						
	Slot-5	A					
	Slot-6			Y			Z
	Slot-7						
	Slot-8						

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat	
	Slot-1	D					Е	
	Slot-2	С	Z	A	D	Е	A	
C4 (ECE-G22)	Slot-3	A		Е	С	A		
(ECE-G22)	Slot-4	В		В	В	D	W	
		LUNCH BREAK						
	Slot-5							
	Slot-6	X			Υ			
	Slot-7							
	Slot8							

	Time
Slot-1	09:00 AM to 09:55 AM
Slot-2	10:00 AM to 10:55 AM
Slot-3	11:00 AM to 11:55 AM
Slot-4	12:00 PM to 12:55 PM
LU	JNCH BREAK
Slot-5	2.00 PM to 2:55 PM
Slot-6	3.00 PM to 3:55 PM
Slot-7	4.00 PM to 4:55 PM
Slot-8	5.00PM to 5.55PM

	Course code	Course Name	L-T-P	TOTAL CONTACT HOURS	FACULTY
А	MA1101	Linear Algebra&Calculus	3-1-0	4	Guest Faculty(C1,C2,C3,C4)
В	EC1101	Digital Electronic Circuits	3-0-0	3	Mr.Md.Arif Ali(c1,c2,c3)+Mrs.Ch.Ramya(c4)
X	EC1701	Digital Electronic Circuits Lab	0-0-3	3	Mr.A.Rajkumar(C1-A,C2-A,C3-A,C4- A)+Mrs.Ch.Ramy(c1-B,C2-B,C3-B,c4-B)
С	ME1102	Introduction to Manufacturing Processes Theory	0-2-0	2	Mr. K. Ravi Kumar (C1, C2, C3, C4)
Z	ME1102	Introduction to Manufacturing Processes LAB	0-0-2	2	Guest Faculty(C1, C2,C3,C4)
D	CS1101	Programming for Problem Solving	3-0-0	3	Mrs.U.Nagamani(C1)+Mr.B.VenkataRaman(C2)
Y	CS1701	Programming for Problem Solving Lab	0-0-3	3	Mr. Sujoy sarkar(C4)+ , Mr.B VenkatRaman(C2,C3)+Ms.Latha(C1)
Е	BM0005	Constitution of India	3-0-0	3	Ch.Sophia Rani(C1,C2,C3,C4)
W	EC	Element of Electronics and Communications Engineering	0-0-2	2	

(Mr.R.Saidulu)

(Mr.B.Bhavsingh)

(Mr.K.Karthik)

(Mrs.G.Srujana)

Faculty I/c, Dept. Timetables

(Faculty I/c, Overall Timetables)

HOD/ECE

Associate Dean Engineering