

Rajiv Gandhi University of Knowledge Technologies

(Telangana Government Act 8 of 2016) Basar, Nirmal, Telangana State – 504107, India.

A.Y 22-23_E1_SEM1_ELECTRONICS AND COMMUNICATION ENGINEERING TIME TABLE

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat	
	Slot-1	Е	В	С	D			
	Slot-2	A		D	Е			
C1	Slot-3	С	A		A	Z		
(CSE-124)	Slot-4	В	D	Е	В		W	
		LUNCH BREAK						
	Slot-5					A		
·	Slot-6	Y		X				
	Slot-7							
	Slot-8							

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat
	Slot-1			D	Е		C
	Slot-2		В	Е	D	D	
C2	Slot-3	Е	С	A	В	В	
(ECE-F25)	Slot-4	A	A		A		W
	LUNCH BREAK						
	Slot-5						
	Slot-6		Y			X	
	Slot-7						
	Slot-8						

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat
	Slot-1	D	Е			D	
C3	Slot-2	Е	A	С		A	
(ECE-G13)	Slot-3	В	D	В	Е		
	Slot-4	С		A		В	W
	LUNCH BREAK						
	Slot-5	A					
	Slot-6				X		Z
	Slot-7			Y			
	Slot-8						

Year	Time	Mon	Tue	Wed	Th	Fri	Sat
					u		
	Slot-1	D				В	Е
	Slot-2	С	Z	A	D	Е	A
C4	Slot-3	A		Е	С	A	
(ECE-G22)	Slot-4	В		В		D	W
		LUNCH BREAK					
	Slot-5						
	Slot-6		X		Υ		
	Slot-7				<u>. </u>		
	Slot8						

	Time				
Slot-1	09:00 AM to 09:55 AM				
Slot-2	10:00 AM to 10:55 AM				
Slot-3	11:00 AM to 11:55 AM				
Slot-4	12:00 PM to 12:55 PM				
LUNCH BREAK					
Slot-5	2.00 PM to 2:55 PM				
Slot-6	3.00 PM to 3:55 PM				
Slot-7	4.00 PM to 4:55 PM				
Slot-8	5.00PM to 5.55PM				

	Course code	Course Name	L-T-P	TOTAL CONTACT HOURS	FACULTY
Α	MA1101	Linear Algebra&Calculus	3-1-0	4	Guest Faculty(C1,C2,C3,C4)
В	EC1101	Digital Electronic Circuits	3-0-0	3	Mr.Md.Arif Ali(c1,c2,c3)+Mrs.Ch.Ramya(c4)
X	EC1701	Digital Electronic Circuits Lab	0-0-3	3	Mr.A.Rajkumar(C1-A,C2-A,C3-A,C4- A)+Mrs.Ch.Ramy(c1-B,C2-B,C3-B,c4-B)
С	ME1102	Introduction to Manufacturing Processes Theory	0-2-0	2	Mr. K. Ravi Kumar (C1, C2, C3, C4)
Z	ME1102	Introduction to Manufacturing Processes LAB	0-0-2	2	Lab-Guest Faculty(C1, C2,C3,C4)
D	CS1101	Programming for Problem Solving	3-0-0	3	Mr.Rahul(C4)+Guest Faculty2(C1,C2,C3)
Y	CS1701	Programming for Problem Solving Lab	0-0-3	3	Mr. Sujoy sarkar(c1,c2)+ Mr.K Ravikanth(C4)+ , Mr.B VenkatRamana(C3)
Е	BM0005	Constitution of India	3-0-0	3	Ch.Sophia Rani(C1,C2,C3,C4)
W	EC	Element of Electronics and Communications Engineering	0-0-2	2	

(Mr.R.Saidulu)

(Mr.B.Bhavsingh)

(Mr.K.Karthik)
HOD/ECE

(Mrs.G.Srujana)

Faculty I/c, Dept. Timetables (Faculty I/c, Overall Timetables)

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Associate Dean Engineering