



Rajiv Gandhi University of Knowledge Technologies

(Telangana Government Act 8 of 2016)

Basar, Nirmal, Telangana State – 504107, India.

A.Y 25-26_E2_SEM1_ELECTRICAL ENGINEERING TIMETABLE

Class	Time	Mon	Tue	Wed	Thu	Fri	Sat		Class	Time	Mon	Tue	Wed	Th u	Fri	Sat
C1 CSE --124	Slot-1	A	A	B	B	D	Dept. Activity		C2 CSE -126	Slot-1	C	C	A	E	E	E
	Slot-2	Y-B1	Y-B2	D	D	A				Slot-2		A	Y-B1	Y- B2	D	Dept. Activity
	Slot-3			E	A	E				Slot-3	D	D			A	
	Slot-4			C	C	C				Slot-4	B	B			E	
	LUNCH BREAK								LUNCH BREAK							
	Slot-5	B	D	Z-B1 &B2	E	F	Library		Slot-5	F	F	D	Z-B1 &B2	B	Library	
	Slot-6	E	C		F				Slot-6	A		E		C		

Class	Time	Mon	Tue	Wed	Thu	Fri	Sat			Time
C3 CSE -127	Slot-1	D	D	D	C	A	Y-B2		Slot-1	09:00 AM to 09:55 AM
	Slot-2	B	E	C	B	Y-B1			Slot-2	10:00 AM to 10:55 AM
	Slot-3	C	A	B	D				Slot-3	11:00 AM to 11:55 AM
	Slot-4	E	C	E				Slot-4	12:00 PM to 12:55 PM	
	LUNCH BREAK								LUNCH BREAK	
	Slot-5	A	Z-B1& B2	F	A		Library		Slot-5	2.30 PM to 3:25 PM
	Slot-6	F			E				Slot-6	3.30 PM to 4:25 PM

S.No	Course code	Course Title	L-T-P	Contact Hours	Faculty
A	EE2101	Electrical Machines-I	3-1-0	4	Mr. B. Rajesh (C1, C2 & C3)
B	EC2104	Analog Electronic Circuits -II	3-0-0	3	Mrs.P.Tejasvey (C1, C2 & C3)
C	EC2103	Electromagnetic Fields	3-1-0	4	Mr.Anjani Kumar (C1, C2 & C3)
D	ME2105	Engineering Mechanics	3-1-0	4	Mr.Shekar sheelam (C1, C2 & C3)
E	MA2105	Vector Calculus & Complex Analysis	3-1-0	4	Mr.Balaraju (C1,C2 & C3)
F	CS2301	Data Structures	3-0-0	3	Mrs.B. Jyothi (C1,C2 & C3)
Y	EE2701	Electrical Machines-I Lab	0-0-3	3	Mr. B.Rajesh (C1-B1,C2-B1 & C3-B2) Mr. M.Balu (C3-B1), Dr.B.Bhavsingh (C2-B2), (GF) (C1-B2)
Z	EC2701	Analog Electronic Circuits Lab	0-0-2	2	Dr. B. Upender Rao (C3-B2) Mrs. P. Tejasvey (C1-B1, C2-B1&C3-B1). Mrs. Sravanthi (C1-B2&C2-B2)

(Mr. SOBHAN BHUKYA)

Faculty I/c,
Dept. Timetables

(Dr. B. Bhavsingh)
HoD/EE

(Dr. K. Mahesh)
Associate Dean/Engineering