



# Rajiv Gandhi University of Knowledge Technologies

(Telangana Government Act 8 of 2016)

Basar, Nirmal, Telangana State – 504107, India.

Date: 17.08.2024

## A.Y 2024-2025 E2 SEM1 COMPUTER SCIENCE AND ENGINEERING TIMETABLE

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat	Year	Time	Mon	Tue	Wed	Thu	Fri	Sat
C1 CSE	Slot-1	A	C	D				C2 CSE	Slot-1	B	F	A	E		
	Slot-2	C	D	C	A	B			Slot-2	D	C	D	B		
	Slot-3	E	E	E	F	B			Slot-3	F	D	F	C	A	
	Slot-4	D	F	F	B	A			Slot-4	E	E	C	A	B	
	LUNCH BREAK								LUNCH BREAK						
	Slot-5	Z	X	Y					Slot-5	X	Z		Y		
	Slot-6	Z	X	Y					Slot-6	X	Z		Y		
	Slot-7	Z		Y					Slot-7		Z		Y		
	Slot-8								Slot-8						
C3 CSE	Slot-1	E	A	A	E			C4 CSE	Slot-1	A	E	B	A	B	
	Slot-2	D	F	D	F	F			Slot-2	E	D	F	C		
	Slot-3	C	D	C	A				Slot-3	D	C	E	F	F	
	Slot-4	B	B	B	C	E			Slot-4	C	B	D			
	LUNCH BREAK								LUNCH BREAK						
	Slot-5	X	Y	Z					Slot-5	Y	X		Z		
	Slot-6	X	Y	Z					Slot-6	Y	X		Z		
	Slot-7		Y	Z					Slot-7	Y			Z		
	Slot-8								Slot-8						

	<b>Course code</b>	<b>Course Name</b>	<b>L-T-P</b>	<b>TOTAL HOURS</b>	<b>FACULTY</b>
A	BM2101	Managerial Economics and financial Analysis	3-1-0	4	Srividya (C1,C2) Guest Faculty (C3,C4)
B	EC2101	Digital Electronic Circuits	3-0-0	3	Mrs.P.Tejasvey(C1, C2, C3) Guest faculty (C4)
C	CS2101	Database Management System	3-0-0	3	D.Devyani (C1,C2,C3,C4)
D	CS2102	Object Oriented Programming	0-0-3	3	P.Laxmi Narayana (C1,C2) Latha (C3,C4)
E	CS2103	Computer Organisation and Architecture	3-0-0	3	P.Sarika (C1,C2) Guest faculty (C3,C4)
F	HS2101	Essence of Indian Traditional Knowledge	0-0-3	3	A.srinivas (C1,C2,C3,C4)
X	EC2701	Digital Electronic Circuits Lab	0-0-2	2	Mrs.B.Neelima (C1-A),(C2-A) Mr.R.Saidulu (C1-B) Mr.Ch.Shravan (C2-B) Mrs.P.Tejasvey (C3-A),(C4-A)
Y	CS2701	Database Management Lab	0-0-3	3	D.Devyani (C1,C2) R.Rahul (C3,C4)
Z	CS2702	Object Oriented Programming Lab	0-0-3	3	P.LaxmiNaryana (C1,C2) latha (C3,C4)

**Time Table**

**I/c**

**Mr. VENKAT RAMAN B**

**HOD/CSE**

**Mr. K. MAHESH**

**Associate Dean Engineering**