



# Rajiv Gandhi University of Knowledge Technologies

(Telangana Government Act 8 of 2016)

Basar, Nirmal, Telangana State – 504107, India.

## A.Y 22-23\_E1\_SEM2\_ELECTRICAL ENGINEERING TIMETABLE

Class	Time	Mon	Tue	Wed	Thu	Fri	Sat	Class	Time	Mon	Tue	Wed	Thu	Fri	Sat				
	Slot-1	A	Z, B <sub>1</sub>	C	A	D	B		Slot-1	C	X B <sub>1</sub> ,B <sub>2</sub>	F	C	A	Z				
	Slot-2	F		A	F			B <sub>1</sub>		Slot-2		A	C	A	D				
<b>C1</b> CSE-012	Slot-3	D	Z, B <sub>2</sub>	B	C	F		<b>C2</b> CSE-127	Slot-3	B	F	B	D	E	Z				
	Slot-4	E			C	E	D		B <sub>2</sub>										
<b>LUNCH BREAK</b>								<b>LUNCH BREAK</b>											
	Slot-5	X B <sub>1</sub> ,B <sub>2</sub>		W B <sub>1</sub> ,B <sub>2</sub>		Y B <sub>1</sub> ,B <sub>2</sub>			Slot-5		Y B <sub>1</sub> ,B <sub>2</sub>	W B <sub>1</sub> ,B <sub>2</sub>	D						
	Slot-6												Slot-6						
	Slot-7												Slot-7						

Class	Time	Mon	Tue	Wed	Thu	Fri	Sat	Time		
	Slot-1	F	C	A	F	B		Slot-1	09:00 AM to 09:55 AM	
	Slot-2	C	A	F	C			Slot-2	10:00 AM to 10:55 AM	
<b>C3</b> CSE-128	Slot-3	E	B	E				Slot-3	11:00 AM to 11:55 AM	
	Slot-4	D		D	A	D		Slot-4	12:00 PM to 12:55 PM	
<b>LUNCH BREAK</b>								<b>LUNCH BREAK</b>		
	Slot-5		X B <sub>1</sub> ,B <sub>2</sub>	W B <sub>1</sub> ,B <sub>2</sub>	Y B <sub>1</sub> ,B <sub>2</sub>	Z		Slot-5	2.00 PM to 2:55 PM	
	Slot-6					Z <sub>B1</sub>			Slot-6	3.00 PM to 3:55 PM
	Slot-7					Z <sub>B2</sub>			Slot-7	4.00 PM to 4:55 PM

S.No	Course code	Course Title	L-T-P	Contact Hours	Faculty
A	EE1201	Network Theory-2	3-0-0	3	Mr. K. Nagachari (C1,C2 & C3)
B	MA1202	Differential Equations & Laplace Transforms	3-1-0	4	Mr.T. Prakash (C1,C2 & C3)
C	CY1201	Engineering Chemistry	3-0-0	3	Dr.K.Pavani(C1,C2 & C3)
D	CS1101	Programming for Problem Solving	3-0-0	3	Mr. B.Thilak Reddy(C1,C2 & C3)
E	BM005	Constitution of India	2-0-0	2	Mr.Swapnil (C1,C2 & C3)
F	EC12XX	Analog Electronic Circuits-1	3-0-0	3	Mr. GVSSKR NAGANJANEYULU(C1,C2 & C3)
W	EE1801	Network Theory Lab	0-0-3	3	Mr.K.Nagachari (C1-B1,C2-B1& C3-B1) Mr.M.Praveenkumar (C1-B1,C2-B1 & C3-B1)
X	CY1801	Engineering Chemistry Lab	0-0-3	3	Dr.K.Pavani (C1,C2 & C3)
Y	CS1701	Programming for Problem Solving Lab	0-0-3	3	Mr. B.Thilak Reddy(C1,C2), Mr.B.Srikanth(C3)
Z	ME1203	Engineering workshop	0-1-2	3	Mr. Siddaiah (C1), Mr.Ravi Kumar (C2). Mrs. Ashwini (C3)

(Mr.S.Mahesh)  
Faculty I/c,  
Dept. Timetables

(Mr.B.Bhavsingh)  
Faculty I/c,  
Overall Timetables

(Mr.V.Vinay Kumar)  
HoD/EE

(Mrs.G.Srujana)  
Associate Dean/Engineering