



Rajiv Gandhi University of Knowledge Technologies

(Telangana Government Act 8 of 2016)

Basar, Nirmal, Telangana State – 504107, India.

A.Y 22-23_E2_SEM2 ELECTRONICS AND COMMUNICATION ENGINEERING TIME TABLE

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat		Year	Time	Mon	Tue	Wed	Thu	Fri	Sat
C1(CSE-011) Dept. Building	Slot-1	I	E	A	D	E		C2(CSE-121) Dept. Building	Slot-1	E	A	I	D	C	G	
	Slot-2	B	B	E	C	C	D		Slot-2	A	E	A	E	I	D	
	Slot-3	F		B	I	F	C		Slot-3	B	B		C	G		
	Slot-4	A	A	I	G				Slot-4	F	I	B	F	F	C	
	LUNCH BREAK								LUNCH BREAK							
	Slot-5	X	G		Y	D	H		Slot-5	Y		X		D	H	
	Slot-6		F						Slot-6							
	Slot-7								Slot-7							

Year	Time	Mon	Tue	Wed	Thu	Fri	Sa t	Year	Time	Mon	Tue	Wed	Th u	Fri	Sat
C3(CS E-122) Dept. Building	Slot-1	A	B	D	E	I	F	C4(CSE -123) Dept. Building	Slot-1	B	I	E	A	F	
	Slot-2	E	A	B	C	G			Slot-2	I	B	D	D	C	F
	Slot-3	D	I	E	F	D	H		Slot-3	C	D	G	G	A	A
	Slot-4	B		A	D	C			Slot-4	D	C	B	E	E	
	LUNCH BREAK								LUNCH BREAK						
	Slot-5	C	F	Y	I	X			Slot-5	F	Y	I	X		H
	Slot-6		G						Slot-6						
	Slot-7								Slot-7						

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat			Time
C5(EC E-G12) Dept. Building	Slot-1	F	F	B	D	C	I		Slot-1	09:00 AM to 09:55 AM
	Slot-2	B	D	I	A	A	A		Slot-2	10:00 AM to 10:55 AM
	Slot-3		C	C	G	E	F		Slot-3	11:00 AM to 11:55 AM
	Slot-4		B	D	E	D			Slot-4	12:00 PM to 12:55 PM
	LUNCH BREAK								LUNCH BREAK	
	Slot-5	E	X	G		Y	H		Slot-5	2.00 PM to 2:55 PM
	Slot-6	I							Slot-6	3.00 PM to 3:55 PM
	Slot-7								Slot-7	4.00 PM to 4:55 PM

	Course code	Course Name	L-T-P	TOTAL CONTACT HOURS	FACULTY
A	EC2201	Analog Circuits	3-0-0	3	Mr.R.Saidulu(C1,C2,C3), Mrs.P.Tejasvey(C4,C5)
X	EC2801	Analog Circuits lab	0-0-2	2	Mr.V S S K R Naganjaneyulu(C1(A),C3(A),C4(A)) Mr.K .Karthik (C5(A), Mr.B.Uppender (C1(B),C2(B),C5(B),C4(B))Mrs.B.Neelima(C3(B)), Mrs.P.Tejasvey(C2(A))
B	EC2202	Control systems	3-0-0	3	Mr.Ch.Shravan(C3,C4,C5) ,Mrs.P.Tejasvey(C1,C2)
C	EC2203	Electromagnetic Waves	3-0-0	3	Mrs.B.Neelima(C3,C4,C5), Mrs.P.Pranaya(C1,C2)
D	EC2204	Probability Theory and Stochastic Processes	3-1-0	4	Mr.K .Karthik(C3,C4,C5)+Ms.R.Padmasree(C1) Mr.A.Sainath Chaithanya(C2)
E	CS2205	Object Oriented Programming	3-0-0	3	Mr.K.Ravikanth(C1,C2,C3), Mr.M Surender(C4),Mrs.Latha(C5)
Y	CS2804	Object Oriented Programming lab	0-0-2	2	Mr.K.Ravikanth(C1,C2,C3),Mr.B.Venkat Raman(C4), Guest Faculty(C5)
F	BM0007	Managerial Economics and Financial analysis	3-0-0	3	Guest Faculty(C1,C2,C3,C4,C5)
G	HS2101	Essence of Indian Traditional Knowledge	2-0-0	2	Mrs.K.Kavitha(C1,C2,C3,C4), Mr.K.Venkateshwarlu(C5)
H	EC2902	Technical seminar	0-0-2	2	Mr.Ch.Shravan(C1), Mrs.P.Tejasvey(C2), Mrs.P.Pranaya(C3),Mr.K.Gayatri(C4), Mr.N.Naresh Chary(C5)
I	MAXXXX	Numerical Analysis	3-0-0	3	Mr.B.Sathanna(C1,C2,C3,C4,C5)

(Mr. R.Saidulu)

(Mr.B.Bhavsingh)

(Mr.K.Karthik)

(Mrs.G.Srujana)

Faculty I/c, Dept. Timetables

Faculty I/c, Overall Timetables

HoD/ECE

Associate Dean Engineering