



Rajiv Gandhi University of Knowledge Technologies

(Telangana Government Act 8 of 2016)

Basar, Nirmal, Telangana State – 504107, India.

A.Y 22-23_E2_SEM1_ELECTRICAL ENGINEERING TIMETABLE

Class	Time	Mon	Tue	Wed	Thu	Fri	Sat	Class	Time	Mon	Tue	Wed	Thu	Fri	Sat
	Slot-1	A	C	C	C	B	C		Slot-1	C	D	A	D	C	B
	Slot-2		D	D	D		D								
C1	Slot-3	B	E	A		E		C2	Slot-3	A	A	B	B	Z	E
(CSE-021)	Slot-4		A	E	B		E	(CSE-022)	Slot-4	E	E		E	E	B _{1,B2}
LUNCH BREAK								LUNCH BREAK							
	Slot-5	Z B _{1,B2}	F						Slot-5		F				
	Slot-6								Slot-6						
	Slot-7								Slot-7						

Class	Time	Mon	Tue	Wed	Thu	Fri	Sat	Time	
	Slot-1	D	A	D	B	D	D	Slot-1	09:00 AM to 09:55 AM
	Slot-2	C		C		C	C	Slot-2	10:00 AM to 10:55 AM
C3	Slot-3	E	B	E	E	B		Slot-3	11:00 AM to 11:55 AM
SEMINAR HALL	Slot-4	A		A		E		Slot-4	12:00 PM to 12:55 PM
Dept of EE	LUNCH BREAK							LUNCH BREAK	
	Slot-5			F			Z	Slot-5	2.00 PM to 2:55 PM
	Slot-6						B _{1,B2}	Slot-6	3.00 PM to 3:55 PM
	Slot-7							Slot-7	4.00 PM to 4:55 PM

S.No	Course code	Course Title	L-T-P	Contact Hours	Faculty
A	EE2101	Electrical Machines-I	3-1-0	4	Mr. S. Mahesh (C1, C2 & C3)
B	EE2102	Analog Electronic Circuits	4-1-0	5	Mr.S. Naveen (C1 & C2) Mr. Bhukya Shobhan (C3)
C	EE2103	Electromagnetic Fields	3-1-0	4	Guest Faculty (C1, C2 & C3)
D	EE2104	Engineering Mechanics	3-1-0	4	Mrs.Ashwini (C1, C2 & C3)
E	MA2105	Vector Calculus & Complex Analysis	3-1-0	4	Guest Faculty (C1, C2 & C3)
F	HS2101	Essence of Indian Traditional Knowledge	0-0-2	2	Mr. K.Krushna Prasad (C1) Mr.G.Shankar (C2 & C3)
Z	EE2701	Analog Electronic Circuits Lab	0-0-2	2	Mr.S. Naveen(C1-B1,C2-B1&C3-B1) , Guest Faculty (C1-B2 & C2-B2) Mr. Bhukya Shobhan (C3-B2)

(S.MAHESH)

Faculty I/c,
Dept.Timetables

Faculty I/c,
Overall Timetables

HoD/EE

Associate Dean Engineering