



Rajiv Gandhi University of Knowledge Technologies

(Telangana Government Act 8 of 2016)

Basar, Nirmal, Telangana State – 504107, India.

A.Y 23-24_E2_SEM1_ELECTRICAL ENGINEERING TIMETABLE

Class	Time	Mon	Tue	Wed	Thu	Fri	Sat	Class	Time	Mon	Tue	Wed	Thu	Fri	Sat			
	Slot-1	A	E	E	C	F	F		Slot-1	E	B	A	E	D	D			
	Slot-2	B	C	C	E	D	D		Slot-2	A	A	B	A					
C1 CSE-012	Slot-3	C	A	A	A	Z	D	C2 CSE-127	Slot-3	B	E	E	C	Y	Y			
	Slot-4	E	B	B	D	B _{1,B2}			Slot-4	C	C	C	F	B ₁	B ₂			
LUNCH BREAK								LUNCH BREAK										
	Slot-5	Y B ₁	Y B ₂	Dept - Act	F				Slot-5	D	F	Dept - Act	Z B _{1,B2}	F				
	Slot-6										Slot-6			D				
	Slot-7										Slot-7							

Class	Time	Mon	Tue	Wed	Thu	Fri	Sat	Time	
	Slot-1	C	A	B	A	E		Slot-1	09:00 AM to 09:55 AM
	Slot-2	E	B	A	Y B ₁	C	D	Slot-2	10:00 AM to 10:55 AM
C3 CSE-128	Slot-3	A	C	C		D	F	Slot-3	11:00 AM to 11:55 AM
	Slot-4	B	E	E	F		Slot-4	12:00 PM to 12:55 PM	
LUNCH BREAK								LUNCH BREAK	
	Slot-5	F	D	Dept - Act	Y B ₂	Z		Slot-5	2.00 PM to 2:55 PM
	Slot-6		D			B _{1,B2}		Slot-6	3.00 PM to 3:55 PM
	Slot-7							Slot-7	4.00 PM to 4:55 PM

S.No	Course code	Course Title	L-T-P	Contact Hours	Faculty
A	EE2102	Electrical Machines-I	3-1-0	4	Mr.K.Nagachari(C1, C2 & C3)
B	EC2101	Analog Electronic Circuits -II	3-0-0	3	Mr.VSSKRNaganjaneyulu(C1, C2 & C3)
C	EC2103	Electromagnetic Fields	3-1-0	4	Mr.B.Upender (C1, C2 & C3)
D	ME2104	Engineering Mechanics	3-1-0	4	Dr. Tirupathi (C1, C2 & C3)
E	MA2105	Vector Calculus & Complex Analysis	3-1-0	4	Mr.Balraj(C1,C2 & C3)
F	CS2301	Data Structures	3-0-0	3	Mr.(C1,C2 & C3)
Y	EE2702	Electrical Machines-I Lab	0-0-3	3	Mr. K.Nagachari(C1-B1&B2 & C2-B1) Mr.T.Naveen Kumar(C2-B2)(C3-B1&B2)
Z	EC2701	Analog Electronic Circuits Lab	0-0-2	2	Mr.A.Rajkumar (C1-B1&B2) Mr. VSSKR Naganjaneyulu (C2-B1) Mrs.Ch.Ramya(C2-B2),Mr.S.Naveen(C3-B1) Mr.Anjani Kumar(C3-B2)

(Mr.T.Naveen Kumar)
Faculty I/c,
Dept. Timetables

(Mr.B.Bhavsingh)
Faculty I/c,
Overall Timetables

(Mr.V.Vinay Kumar)
HoD/EE

(Mrs. G. Srujana)
Associate Dean/Engineering