



Rajiv Gandhi University of Knowledge Technologies

(Telangana Government Act 8 of 2016)

Basar, Nirmal, Telangana State – 504107, India.

A.Y 22-23_E1_SEM1 ELECTRONICS AND COMMUNICATION ENGINEERING TIME TABLE

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat
C1 (CSE-124)	Slot-1	E		C	D		
	Slot-2	A	X	D	E		W
	Slot-3	C			A	Z	
	Slot-4	B			B		
	LUNCH BREAK						
	Slot-5		D		F	B	
	Slot-6	Y	A			A	
	Slot-7						
	Slot-8						

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat
C2 (ECE-F25)	Slot-1		F		D	E	C
	Slot-2	X	B	E		D	
	Slot-3		C	A	B	B	
	Slot-4	A	A		A		
	LUNCH BREAK						
	Slot-5	D					
	Slot-6		Y				W
	Slot-7				Z		
	Slot-8						

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat
C3 (ECE-G13)	Slot-1	F			D	D	
	Slot-2	E	D	C		A	
	Slot-3	B	A	B	X	E	
	Slot-4	C		A		B	W
	LUNCH BREAK						
	Slot-5	A					Z
	Slot-6			Y			
	Slot-7						
	Slot-8						

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat
C4 (ECE-G22)	Slot-1	D		D	F		A
	Slot-2	C	Z	A	D	E	
	Slot-3	A		E	C	A	
	Slot-4	B		B	B		
	LUNCH BREAK						
	Slot-5						
	Slot-6	X	Y				
	Slot-7						
	Slot-8						W

	Time
Slot-1	09:00 AM to 09:55 AM
Slot-2	10:00 AM to 10:55 AM
Slot-3	11:00 AM to 11:55 AM
Slot-4	12:00 PM to 12:55 PM
LUNCH BREAK	
Slot-5	2.00 PM to 2:55 PM
Slot-6	3.00 PM to 3:55 PM
Slot-7	4.00 PM to 4:55 PM
Slot-8	5.00PM to 5.55PM

	Course code	Course Name	L-T-P	TOTAL CONTACT HOURS	FACULTY
A	MA1101	Linear Algebra&Calculus	3-1-0	4	Mr.D.Raju(C1,C2,C3,C4)
B	EC1101	Digital Electronic Circuits	3-0-0	3	Mr.Md.Arif Ali(c1,c2,c3)+Mrs.Ch.Ramya(c4)
X	EC1701	Digital Electronic Circuits Lab	0-0-3	3	Mr.A.Rajkumar(C1-A,C2-A,C3-A,C4-A)+Mrs.Ch.Ramya(c1-B,C2-B,C3-B,c4-B)
C	ME1102	Introduction to Manufacturing Processes Theory	0-2-0	2	Mr. K. Ravi Kumar (C1, C2, C3, C4)
Z	ME1102	Introduction to Manufacturing Processes LAB	0-0-2	2	Mr.Tirupathi(C1, C2)+Mr.Siddaiah(C3,C4)
D	CS1101	Programming for Problem Solving	3-0-0	3	Mrs.U.Nagamani(C1)+Mr.B.Thilak(C3)+Mr.A.Sreedhar(C2,C4)
Y	CS1701	Programming for Problem Solving Lab	0-0-3	3	Mrs.Latha, Mr.A.Sreedhar (C4)+, Mr.K.Ravikanth, Mr.A.Sreedhar (C2), +Mr.Sujoy Sarkar , Mr.B.Thilak (C3)+Mrs.RajyaLaxmi, Mr.B.Thilak(c1)
E	BM0005	Constitution of India	2-0-0	2	Ch.Sophia Rani(C1,C2,C3,C4)
W	EC.....	Element of Electronics and Communications Engineering	0-0-2	2	Mr.V S S K R Naganjaneyulu
F		Counseling Class			Mr. Shiva

(Mr.R.Saidulu)

Faculty I/c, Dept. Timetables

(Mr.B.Bhavsingh)

(Faculty I/c, Overall Timetables)

(Mr.K.Karthik)

HOD/ECE

(Mrs.G.Srujana)

Associate Dean Engineering