**ECE Dept. Remedial lab exams time table**

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| **S.No** | **Lab Name** | **Date** | **Session** | **Venue** | **Faculty** |
| 1 | Basic Electronics Lab & Electronic circuits Lab | 05-02-18 | After noon | Basic Electronics Lab | A.Dheeraj/R.Saidulu |
| 2 | Digital Logic Design Lab | 06-02-18 | Morning | Digital Logic Design Lab | Revya naik/ascherya |
| 3 | RFMW Lab | 06-02-18 | Morning | RFMW Lab | VSSKR Naganjaneyulu/ Upender |
| 4 | Analog Communication Lab | 07-02-18 | Morning | AC/DC Lab | Madhu |
| 5 | Digital Signal Processing Lab | 05-02-18 | Morning | DSP LAB(203) | Karthik |
| 6 | MPMC LAB | 06-02-18 | Morning | MPMC LAB(203) | Sainath |
| 7 | SCD LAB | 06-02-18 | Morning | SCD LAB | G.Sai radhakrishna |
| 8 | Network Analysis Lab | 07-02-18 | Morning | SCD/NA LAB | Vinay |
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| **Timings:** | Morning 9:30AM – 12:30PM |
| After noon: 2:00PM – 5:00PM |