



# Rajiv Gandhi University of Knowledge Technologies

(Telangana Government Act 8 of 2016)

Basar, Nirmal, Telangana State – 504107, India.

## A.Y 22-23\_E2\_SEM1\_COMPUTER SCIENCE AND ENGINEERING TIMETABLE

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat	Year	Time	Mon	Tue	Wed	Thu	Fri	Sat
C1 (AB-II-311)	Slot-1	A	C	B	D	E	B	C2 (AB-II-313)	Slot-1	B	F	A	E	C	y
	Slot-2	C	A	E	E	D			Slot-2	A	C	C	D	E	
	Slot-3	D		A	C	C			Slot-3	C	A	E	B	D	
	Slot-4	B	E	F	B	F			Slot-4			B	F		
	LUNCH BREAK								LUNCH BREAK						
	Slot-5	X	Y				Z		Slot-5	E	X	D	Z		
	Slot-6									Slot-6			B		
Slot-7								Slot-7							

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat	Year	Time	Mon	Tue	Wed	Thu	Fri	Sat
C3 (AB-II-314)	Slot-1	E	B	C	F	B		C4 (AB-I-102)	Slot-1	C	E	F	B	D	E
	Slot-2	D		A	B	C			Slot-2	E	B	C	C	A	A
	Slot-3	A	E	B	D	E			Slot-3	D	C	D	E	B	
	Slot-4	C	A	E	C	D			Slot-4	A	F				
	LUNCH BREAK								LUNCH BREAK						
	Slot-5	F	Z	X	Y				Slot-5	B	Z	Y		X	
	Slot-6												Slot-6		
Slot-7									Slot-7						

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat	Time		
C5 (AB-I-104)	Slot-1		A		C	F	A	Slot-1	09:00 AM to 09:55 AM	
	Slot-2	C	C	F	B	D	E	Slot-2	10:00 AM to 10:55 AM	
	Slot-3	E	B	C		A		Slot-3	11:00 AM to 11:55 AM	
	Slot-4	D		D	E	B		Slot-4	12:00 PM to 12:55 PM	
	LUNCH BREAK							LUNCH BREAK		
	Slot-5		E	Z	X	Y		Slot-5	2.00 PM to 2:55 PM	
	Slot-6	B								Slot-6
Slot-7									Slot-7	4.00 PM to 5:55 PM

	Course code	Course Name	L-T-P	TOTAL CONTACT HOURS	FACULTY
A	CS2101	Data Structure & Algorithms	3-0-0	3	Mr.Laxmi Narayana.P(C1,C2,C3,C4,C5)
B	CS2102	Discrete Mathematics	4-0-0	4	GUEST Faculty1(C1,C2,C3), Mr. Sujoy sarkar (C4,C5)
C	EC2105	Analog Electronic circuits	4-0-0	4	Mr.Saidulu.R(C1,C2,C3),Ms.Tejaswini(C4,C5)
D	EC2101	Digital Electronic Circuits	3-0-0	3	Mr.Deeraj(C1,C2,C3),Mr.Raj Kumar(C4,C5)
E	MA2102	Probability and Statistics	4-0-0	4	Mr.Thirupathi.G(C1,C2,C3,C4,C5)
F	HS2101	Essence of Indian Traditional Knowledge	2-0-0	2	K. Kavitha Rani(C1,C2), K. Venkateshwarlu (C3,C4,C5)
X	CS2701	Data Structure & Algorithms Lab	0-0-4	4	Ms.U.Nagamani(C1,C2), Ms.Geeta.K(C3,C4,C5)
Y	EC2703	Analog Electronics Circuits Lab	0-0-3	3	Mr.Saidulu.R(C1(A),C3(A)), Ms.B Nilima(C3(B),C4(A),C5(A)), GF2(C1(B),C2(B),C4(B)),GF5(C2(A),C5(B))
Z	EC2701	Digital Electronic Circuits Lab	0-0-2	2	Mr.Deeraj(C1(A),C2(A),C3(A)),Mr.Raj Kumar(C1(B),C4(A),C5(A)), MR. MD. Arif ali (C2(B).C3(B),C4(B),C5(B))

HoD/CSE

Associate Dean Engineering

Faculty I/c,  
Overall Timetables

Faculty I/c,  
Dept. Timetables