



# Rajiv Gandhi University of Knowledge Technologies

(Telangana Government Act 8 of 2016)

Basar, Nirmal, Telangana State – 504107, India.

## A.Y 23-24\_E3\_SEM1\_ELECTRICAL ENGINEERING TIMETABLE

Class	Time	Mon	Tue	Wed	Thu	Fri	Sat	Class	Time	Mon	Tue	Wed	Thu	Fri	Sat
	Slot-1	A	A	A	A	D			Slot-1	C	C	C	C	E	D
	Slot-2	B	B	B	B	E			Slot-2	A	A	A	A		
C1 (CSE-021)	Slot-3	C	C	C	C	Z-B2		C2 (CSE-022)	Slot-3	B	B	B	B	X-B1 & Y-B2	X-B2 & Y-B1
	Slot-4	D	D	E	E		Slot-4		E	E	D	D			
<b>LUNCH BREAK</b>								<b>LUNCH BREAK</b>							
	Slot-5	Z-B1	X-B1 & Y-B2		X-B2 & Y-B1	F			Slot-5		Z-B1		F	Z-B2	
	Slot-6			Slot-6											
	Slot-7			Slot-7											

Class	Time	Mon	Tue	Wed	Thu	Fri	Sat	<b>Almanac</b>			
	Slot-1	B	B	D	D	C	C	Commencement of semester-I class work: 23.08.2023			
	Slot-2	B	B	C	C	D	E	Last day of instruction for semester-I: 06.12.2023			
C3 SEMINAR HALL	Slot-3	A	A	A	A	E	E	MT's exam date		Assignments submission date	
	Slot-4										
<b>LUNCH BREAK</b>								MT-1	22-09-2023 23-09-2023	A-1	19-09-2023
	Slot-5	X-B1 & Y-B2	F		Z-B1	X-B2 & Y-B1	Z-B2	MT-2	03-11-2023 04-11-2023	A-2	30-10-2023
	Slot-6			MT-3				07-12-2023 08-12-2023	A-3	04-12-2023	
	Slot-7										

S.No	Course code	Course Title	L-T-P	Contact Hours	Faculty
A	EE3101	Power Systems-II	3-1-0	4	Mr. B.Bhavsingh (C1, C2 & C3)
B	EE3102	Control Systems	3-1-0	4	Dr. Namani Rakesh (C1 & C2 ) Mr. Sobhan Bhukya (C3)
C	EE3103	Electrical Measurements & Instrumentation	3-1-0	4	Mr.B. Rajesh (C1, C2 & C3)
D	EE3104	Micro Processors	3-0-0	3	Guest Faculty (C1, C2 & C3)
E		Open Elective-I ( OOP )	3-0-0	3	Ms D. Devyani (C1, C2 & C3)
F		Seminar-1 (Technical)	2-0-0	2	Mr B. Rajesh (C1) Mr T Naveen Kumar (C2) Mr B.Ravi Kumar (C3)
X	EE3701	Electrical Machines-II Lab	0-0-3	3	Mr.S.Mahesh (C1-B1 & B2) & (C3-B1 & B2) Mr.B.Rajesh (C2-B1 & B2)
Y	EE3702	Electrical Measurements & Inst. Lab	0-0-2	2	Mr.V.Vinay Kumar (C1-B1 & B2) Mr. B.Bhavsingh (C2-B1 & B2) & (C3-B1) Mr.B.Rajesh (C3-B2)
Z	EE3703	Micro Processors Lab	0-0-2	2	Guest Faculty C1, C2,C3)

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### **A.Y 22-23\_E1\_SEM2\_ELECTRICAL ENGINEERING TIMETABLE**

SLOTS	TIME
Slot-1	09:00 AM to 09:55 AM
Slot-2	10:00 AM to 10:55 AM
Slot-3	11:00 AM to 11:55 AM
Slot-4	12:00 PM to 12:55 PM
<b>LUNCH BREAK</b>	
Slot-5	2.00 PM to 2:55 PM
Slot-6	3.00 PM to 3:55 PM
Slot-7	4.00 PM to 4:55 PM

### **Department Activities (Dept. Act) on Wednesday for C1,C2 & C3**

- Week-1:      Talk on Career Opportunities for Electrical Engineering**
- Week-2.      Technical Events**
- Week-3.      Mana RGUKT- Mana Bhadhyatha**
- Week-4.      Sports and Games**
- Week-5.      Placement Orientation**
- Week-6.      Interaction session with Student Counseling Cell**
- Week-7.      Interaction session with Department Faculty**
- Week-8.      Alumni Talk**
- Week-9.      Mana RGUKT- Mana Bhadhyatha**
- Week-10.      Placement Preparation Guidance**
- Week-11.      Competitive exams preparation guidance**
- Week-12.      Session about women empowerment**
- Week-13.      Quiz Competition**
- Week-14.      Guest Talk**

**(Mr.T.Naveen kumar)**  
Faculty I/c,  
Dept. Timetables

**(Mr.B.Bhavsingh)**  
Faculty I/c,  
Overall Timetables

**(Mr.V.Vinay Kumar)**  
HoD/EE

**(Mrs.G.Srujana)**  
Associate Dean/Engineering

**(Mr.S.Mahesh)**  
Faculty I/c,  
Dept. Timetables