



A.Y 22-23_E3_SEM1_ELECTRONICS AND COMMUNICATION ENGINEERING TIME TABLE

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat		Year	Time	Mon	Tue	Wed	Thu	Fri	Sat
C1(ABII-014)	Slot-1		A	F	C	E		C2(ABII-212)	Slot-1			A		A	C	
	Slot-2	A				C			Slot-2	B	A	F	F	E	D	
	Slot-3	B	B	A	X(B)	B	W		Slot-3	X(B)/	C	D	B	F	W	
	Slot-4	C	D	D	/Z(A)	F			Slot-4	Z(A)	B		E	C		
	LUNCH BREAK								LUNCH BREAK							
	Slot-5	X(A)	E	E	D	Y			Slot-5	E	X(A)/	Y				
	Slot-6	/Z(B)	F						Slot-6	D	Z(B)					
	Slot-7								Slot-7							

Year	Time	Mon	Tue	Wed	Thu	Fri	Sa t		Year	Time	Mon	Tue	Wed	Th u	Fri	Sat
C3(ABII-213)	Slot-1		F	E	F	C		C4(ABII-214)	Slot-1	F	E	D	D	B		
	Slot-2	C	E	D	B	D	W		Slot-2	A	F	A	E	E	W	
	Slot-3	D	X(B)	A	A	E			Slot-3	C	A	F	C	X(B)/		
	Slot-4	B	/Z(A)	C	A	B			Slot-4	D	B	B		Z(A)		
	LUNCH BREAK								LUNCH BREAK							
	Slot-5	F		X(A)/	Y				Slot-5		Y		X(A)	C		
	Slot-6			Z(B)					Slot-6				/Z(B)			
	Slot-7									Slot-7						

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat		Time
C5(ABII-301)	Slot-1		D	C	B	D		Slot-1	09:00 AM to 09:55 AM
	Slot-2	F	A	E	D	B		Slot-2	10:00 AM to 10:55 AM
	Slot-3	A	E	X(B)/Z(A)	E	C	W	Slot-3	11:00 AM to 11:55 AM
	Slot-4	B	C			A		Slot-4	12:00 PM to 12:55 PM
	LUNCH BREAK							LUNCH BREAK	
	Slot-5		F		F	X(A)/Z(B)		Slot-5	2.00 PM to 2:55 PM
	Slot-6	Y						Slot-6	3.00 PM to 3:55 PM
	Slot-7							Slot-7	4.00 PM to 4:55 PM

	Course code	Course Name	L-T-P	TOTAL CONTACT HOURS	FACULTY
A	EC3101	Analog and Digital Communications	3-0-0	3	Mr.K .Karthik(C1,C2)+Mr.S.Naveen (C3,C4,C5)
B	EC3102	Computer Architecture	3-0-0	3	Mrs.B.Neelima(C1,C2,C3)+Mr.S.Naveen(C4,C5)
C	EC3103	Digital signal Processing	3-0-0	3	Mr.N.Naresh Chary(C1,C2,C3,C4,C5)
D	EC3104	RF and Microwave Engineering	3-0-0	3	Ms.R.Padmasree(C3,C4,C5)+Mrs.Ch.Ramya(C1,C2)
E	EC3105	VLSI Engineering	3-0-0	3	Mr.A.Sainath Chaithanya(C3,C4,C5)+Mrs.M Santhoshi(C1,C2)
F	MA3101	Numerical Methos (Open Elective)	3-0-0	3	Mr. B Sathanna(C1,C2,C3,C4,C5)
X	EC3701	Analog and Digital Communications Lab	0-0-2	2	Mr.K.Karthik(C2(B),C1(B),C4(B),C2(A),C3(A),C5(A))Mrs. P.Tejasvey(C3(B),C5(B),C1(A),C4(A))
Y	EC3702	Digital Signal Processing Lab	0-0-2	2	Mr.V S S K R Naganjaneyulu (C4(A),C2(B),C5(A))Mr.A.Sainath Chaithanya (C5(B),C4(B),C3(B),C1(B), Mrs.S. P.Pranaya(C1(A),C2(A),C3(A))
Z	EC3703	RF and Microwave Engineering Lab	0-0-2	2	Mr.Ch.Shravan(C1(B),C2(A),C3(B),C2(B),C5(A)),Ms.R.Padmasree(C4(B),C5(B),C3(A),C1(A),C4(A))
W	EC3901	Seminar-III	0-0-2	2	

(Mr.R.Saidulu)

Faculty I/c, Dept. Timetables

(Mr.B.Bhavsingh)

Faculty I/c, Overall Timetables

(Mr.K.Karthik)

HoD/ECE

(Mrs.G.Srujana)

Associate Dean Engineering