



**A.Y 22-23\_E3\_SEM2\_ELECTRONICS AND COMMUNICATIONENGINEERING TIMETABLE**

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat	Year	Time	Mon	Tue	Wed	Thu	Fri	Sat
C1(ABII-004)	Slot-1	C	C	B	D		E	C2(ABII-212)	Slot-1	D	A	D	B	D	
	Slot-2		E			X(B)	D		Slot-2			A		E	X(B)
	Slot-3	B		A			F		Slot-3	E		C			
	Slot-4	A	A	C	B				Slot-4	C	C	B		B	
	LUNCH BREAK								LUNCH BREAK						
	Slot-5	X(A)	Y	D		E			Slot-5	A		Y	E		F
	Slot-6			E					Slot-6		X(A)				
Slot-7							Slot-7								

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat	Year	Time	Mon	Tue	Wed	Thu	Fri	Sat	
C3(ABII-213)	Slot-1		B	C		B		C4(ABII-214)	Slot-1		B	A	C	C		
	Slot-2	C		E	X(B)		F		Slot-2		X(B)		E			
	Slot-3		C			A			D	Slot-3	C		B		D	F
	Slot-4	B		A			D		Slot-4	B		B	D	A		
	LUNCH BREAK								LUNCH BREAK							
	Slot-5	A		X(A)	Y	D			Slot-5	E	E	E	X(A)	Y		
	Slot-6	E	D			E			Slot-6	D	A	D				
Slot-7							Slot-7									

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat	Time	
C5(ABII-301)	Slot-1		C	B	E	A	D	Slot-1	09:00 AM to 09:55 AM
	Slot-2	E	D	X(A)			E	Slot-2	10:00 AM to 10:55 AM
	Slot-3	B	B		C		F	Slot-3	11:00 AM to 11:55 AM
	Slot-4					C		Slot-4	12:00 PM to 12:55 PM
	LUNCH BREAK							LUNCH BREAK	
	Slot-5	Y	A	A	D	X(B)		Slot-5	2.00 PM to 2:55 PM
	Slot-6		E					Slot-6	3.00 PM to 3:55 PM
Slot-7							Slot-7	4.00 PM to 4:55 PM	

	Course code	Course Name	L-T-P	TOTAL CONTACT HOURS	FACULTY
A	EC3201	Micro-Controllers and Interfacing	3-0-0	3	Mr.A.Rajkumar(C4,C5) Ms.R.Padmasree(C1,C2), .Ch.Ramya(C3)
B	EC3202	Digital System Design	3-0-0	3	Mr.A.Dheeraj(C1,C2,C3),Mr.A.Sainath Chaithanya(C4,C5)
C	EC3401	Pattern Recognition	3-0-0	3	Mr.G.Sai Radha Krishna(C3,C4,C5),MrS. P.Pranaya(C1,C2)
D	CS3203	Operating Systems	3-0-0	3	Mr.S Naveen(C1,C2,C3,C4,C5)
E	BM3203	Operations Research	3-0-0	3	Guest Faculty(C1,C2,C3,C4,C5)
F	EC3902	Mini Project	0-0-4	4	
G	EC3000	Comprehensive Viva-I			
X	EC3801	Micro-Controllers lab	0-0-2	2	Mr.A.SainathChaithanya(C3(B),C1(A),C2(A)), Ms.R.Padmasree(C1(B),C3(A),C4(A)) Mr.Md.Arif Ali(C5(A),C2(B),C4(B),C5(B))
Y	EC3802	Digital Systems Design and VLSI Lab	0-0-2	2	Mr.A.Dheeraj(C5(B),C2(B),C4(B)) Mr.Ch.Shravan(C5(A),C1(B),C3(B)), Mr.R.Saidulu(C1(A),C2(A),C3(A),C4(A))

( Mr.R.Saidulu)

Faculty I/c, Dept. Timetables

(Mr.B.Bhavsingh)

Faculty I/c, Overall Timetables

(Mr.K.Karthik)

HoD/ECE

(Mrs.G.Srujana)

Associate Dean Engineering