



Rajiv Gandhi University of Knowledge Technologies

(Telangana Government Act 8 of 2016)

Basar, Nirmal, Telangana State – 504107, India.

A.Y 24-25_E2_SEM1_ELECTRICAL ENGINEERING TIMETABLE

Class	Time	Mon	Tue	Wed	Thu	Fri	Sat	Class	Time	Mon	Tue	Wed	Thu	Fri	Sat
	Slot-1	A	E	E	C	F			Slot-1	E	B	A	D	D	E
	Slot-2	B	C	C	D	D			Slot-2	A	A	B	A	Y B1	
C1 ABII-207	Slot-3	C	A	A	A	Z		C2 ABII-214	Slot-3	B	E	E	C		
	Slot-4	D	B	B	E	B1,B2			Slot-4	C	C	C	F		
LUNCH BREAK								LUNCH BREAK							
	Slot-5	Y B1	Y B2		D	E			Slot-5	D	F		Z	Y B2	
	Slot-6				F	F				Slot-6	F	D	B1,B2		
	Slot-7									Slot-7					

Class	Time	Mon	Tue	Wed	Thu	Fri	Sat	Time	
	Slot-1	C	A	B	A	E		Slot-1	09:00 AM to 09:55 AM
	Slot-2	D	B	A	E	C		Slot-2	10:00 AM to 10:55 AM
C3 ABII-007	Slot-3	A	C	C	F	D		Slot-3	11:00 AM to 11:55 AM
	Slot-4	B	E	E		F		Slot-4	12:00 PM to 12:55 PM
LUNCH BREAK								LUNCH BREAK	
	Slot-5	F	D	Y B2	Y B1	Z B1,B2		Slot-5	2.00 PM to 2:55 PM
	Slot-6	D							Slot-6
	Slot-7						Slot-7	4.00 PM to 4:55 PM	

S.No	Course code	Course Title	L-T-P	Contact Hours	Faculty
A	EE2102	Electrical Machines-I	3-1-0	4	Mr. B. Rajesh (C1, C2 & C3)
B	EC2101	Analog Electronic Circuits -II	3-0-0	3	Mrs. P. Pranaya (C1, C2 & C3)
C	EC2103	Electromagnetic Fields	3-1-0	4	Mrs. Sravanthi(C1, C2 & C3)
D	ME2104	Engineering Mechanics	3-1-0	4	Mrs. Ashwini (C1, C2 & C3)
E	MA2105	Vector Calculus & Complex Analysis	3-1-0	4	Mr. Balaraju(C1,C2 & C3)
F	CS2301	Data Structures	3-0-0	3	Guest faculty (C1,C2 & C3)
Y	EE2702	Electrical Machines-I Lab	0-0-3	3	Mr. Laxman Mutyam(C1-B1& C3-B2) Mr. B. Rajesh(C2-B1 & B2, C3- B1) Mr. B. Ravi Kumar(C1-B2)
Z	EC2701	Analog Electronic Circuits Lab	0-0-2	2	Dr. G. VSSKR Naganjaneyulu (C1-B1) Mrs. P. Pranaya (C1-B2,C2-B2,C3-B2) Mrs. K. Gayathri(C2-B1,C3-B1)

(Mrs. T. Chaitanya)
Faculty I/c,
Dept. Timetables

(Mr. B. Bhavsingh)
HoD/EE

(Mr. K. Mahesh)
Associate Dean/Engineering