



# Rajiv Gandhi University of Knowledge Technologies

(Telangana Government Act 8 of 2016)

Basar, Nirmal, Telangana State – 504107, India.

## A.Y 22-23\_E1\_SEM2\_ELECTRICAL ENGINEERING TIMETABLE

Class	Time	Mon	Tue	Wed	Thu	Fri	Sat	Class	Time	Mon	Tue	Wed	Thu	Fri	Sat			
C1 CSE-012	Slot-1	F	Z	F	D	A	A	C2 CSE-127	Slot-1	A	F	D	E	Z	Z			
	Slot-2	W	B1	A	E	C	D		Slot-2	F	X	F	W	E	B1			
	Slot-3	W B1 B2	Z	E	C	B	B		Slot-3	C	X B1 B2	C	B1 B2	D	Z			
	Slot-4		B2	C	F				Slot-4	D		A		C	B2			
<b>LUNCH BREAK</b>								<b>LUNCH BREAK</b>										
	Slot-5	X B1 B2	Y B1 B2	Dept. Act	Z	D			Slot-5	B	A	Dept. Act	B	Y				
	Slot-6										Slot-6						B1	
	Slot-7										Slot-7						B2	

Class	Time	Mon	Tue	Wed	Thu	Fri	Sat	<b>Almanac</b>			
C3 CSE-128	Slot-1	E	A	A	B	C	D	<b>Commencement of semester-II class work: 05.06.2023</b>			
	Slot-2	Z	F	D		W	A	<b>Last day of instruction for semester-II: 13.09.2023</b>			
	Slot-3	D	B	F	F	B1		<b>MT's exam date</b>		<b>Assignments submission date</b>	
	Slot-4	C		E	C	B2		MT-1	14-07-2023 15-07-2023	A-1	11-07-2023
<b>LUNCH BREAK</b>								MT-2	10-08-2023 11-08-2023	A-2	07-08-2023
	Slot-5		X	Dept.	Y	Z		MT-3	14-09-2023 16-09-2023	A-3	11-09-2023
	Slot-6		B1	Act	B1	B1					
	Slot-7		B2		B2	Z					
	Slot-8					B2					

S.No	Course code	Course Title	L-T-P	Contact Hours	Faculty
A	EE1201	Network Theory-2	3-0-0	3	Mr. K. Nagachari (C1,C2 & C3)
B	MA1202	Differential Equations & Laplace Transforms	3-1-0	4	Mr.T. Prakash (C1,C2 & C3)
C	CY1201	Engineering Chemistry	3-0-0	3	Dr.K.Pavani (C1,C2 & C3)
D	CS1101	Programming for Problem Solving	3-0-0	3	Mr. B.Thilak Reddy(C1,C2 & C3)
E	BM1205	Constitution of India	2-0-0	2	Mr.Swapnil (C1,C2 & C3)
F	EC12XX	Analog Electronic Circuits-1	3-0-0	3	Mr. GVSSKR Naganjaneyulu (C1,C2 & C3)
W	EE1801	Network Theory Lab	0-0-3	3	Mr.K.Nagachari (C1-B1 & C3-B1) Mr.M.Praveen kumar (C1-B2 & C2-B1) Mr.B.Ravi kumar (C2-B2 & C3-B2)
X	CY1801	Engineering Chemistry Lab	0-0-3	3	Dr.K.Pavani (C1,C2 & C3)
Y	CS1701	Programming for Problem Solving Lab	0-0-3	3	Mr. B.Thilak Reddy (C1 & C2) & S.Rajani (C1 & C2), Mr.B.Srikanth (C3) & V.Rajitha (C3)
Z	ME1203	Engineering workshop	0-1-2	3	Mr. Siddaiah (C1), Mr.Ravi Kumar (C2). Mrs. Ashwini (C3)

## A.Y 22-23\_E1\_SEM2\_ELECTRICAL ENGINEERING TIMETABLE

SLOTS	TIME
Slot-1	09:00 AM to 09:55 AM
Slot-2	10:00 AM to 10:55 AM
Slot-3	11:00 AM to 11:55 AM
Slot-4	12:00 PM to 12:55 PM
<b>LUNCH BREAK</b>	
Slot-5	2.00 PM to 2:55 PM
Slot-6	3.00 PM to 3:55 PM
Slot-7	4.00 PM to 4:55 PM

### Department Activities (Dept. Act) on Wednesday for C1,C2 & C3

Week-1:

Week-2.

Week-3.

Week-4.

Week-5.

Week-6.

Week-7.

Week-8.

Week-9.

Week-10.

Week-11.

Week-12.

Week-13.

Week-14.

(Mr.S.Mahesh)  
Faculty I/c,  
Dept. Timetables

(Mr.B.Bhavsingh)  
Faculty I/c,  
Overall Timetables

(Mr.V.Vinay Kumar)  
HoD/EE

(Mrs.G.Srujana)  
Associate Dean/Engineering