

Training and Placement Cell (T&P) RAJIV GANDHI UNIVERSITY OF KNOWLEDGE TECHNOLOGIES Basar Campus, Basar Mandal, Nirmal District.

RGUIIIT-Basar/Placement/Notice Board/149/2019

Date: 05.11.2018

# JOB DESCRIPTION OF EFFTRONICS SYSTEMS PVT LTD

# **Eligibility Criteria:**

X,PUC,B.TECH (CGPA)	7.0 CGPA
BRANCHES	CSE,ECE, ME & CE(B.Tech) ECE & ME(M.Tech)
СТС	3 TO 7 LPA
JOB DESIGNATION	FIRMWARE ENGINEER / SOFTWARE ENGINEER
BACKLOGS	0
BOND	2 Years 6 Months
INTERNSHIP	YES
JOINING LOCATION	Vijayawada

REGISTRATION STARTS FROM: 5<sup>th</sup> Nov 2018,5:00PM

LAST DATE FOR REGISTRATION: 7<sup>th</sup> Nov 2018,2:00PM.

### Firmware Engineer:

Develop products and solutions for future with latest technologies to create continuous value to customers

#### Key Activities:

- Implement embedded software as per comprehensive design & architecture given
- Build basic device drivers
- Explore new technologies through experimental study
- Perform various testing's as per defined test plan and procedures
- Analyse data
- Configure devices
- Derive technical documentation

### Skills Required

- Basic Electronics
- Analog Circuit Design
- Combinational logic circuit
- Sequential logic circuits
- Microcontroller/Microprocessor Systems
- Control Systems
- Communication Standards & Technologies
- Assembly and C programming
- Data structures & Protocols
- Algorithms
- Analytical skills

### Software Engineer:

Develop products and solutions for future with latest technologies to create continuous value to customers

### Key Activities:

- Implement applications as per comprehensive design & architecture given
- Perform various testing's of applications as per defined test plan and procedures
- Explore new technologies through experimental study
- Analyse data
- Configure devices
- Derive technical documentation

## **Skills Required**

- Algorithms
- Data structures
- C Programming, Object Oriented Design & Programming (C++/Java)
- Database Management System
- Data Communications
- Computer Architecture, OS, Software Engineering
- Computer networks & communication
- Protocols
- Web Technologies
- Combinational logic circuit
- Sequential logic circuits
- Analytical skills

Sd/-Placement Office