



**A.Y 22-23\_E3\_SEM2\_ELECTRONICS AND COMMUNICATION ENGINEERING TIME TABLE**

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat	Year	Time	Mon	Tue	Wed	Thu	Fri	Sat	
C1(ABII-014)	Slot-1	C	B	A		D		C2(ABII-212)	Slot-1	A	A	B	E	B	D	
	Slot-2		A	E	D	X	D		Slot-2		C	A	B		X	
	Slot-3	A		D	B		E		Slot-3	C		C	D	E		
	Slot-4	B	C	C					Slot-4	E	D	E		D		
	LUNCH BREAK								LUNCH BREAK							
	Slot-5	X	Y		E				Slot-5		X	Y				
	Slot-6								Slot-6							
Slot-7							Slot-7									

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat	Year	Time	Mon	Tue	Wed	Thu	Fri	Sat
C3(ABII-213)	Slot-1	C		A	B	E		C4(ABII-214)	Slot-1	A	B	B	D	A	E
	Slot-2		B	B	X	D	E		Slot-2	B		C	C		
	Slot-3	B	C			A	D		Slot-3	C	D		A	D	
	Slot-4	D	A	C					Slot-4			D		E	
	LUNCH BREAK								LUNCH BREAK						
	Slot-5		E	X	Y				Slot-5	E			X	Y	X
	Slot-6		D						Slot-6						
Slot-7							Slot-7								

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat	Time	
C5(ABII-301)	Slot-1	E	C	B	A	E		Slot-1	09:00 AM to 09:55 AM
	Slot-2	A		X				Slot-2	10:00 AM to 10:55 AM
	Slot-3	B	B		C	A		Slot-3	11:00 AM to 11:55 AM
	Slot-4	C			D			Slot-4	12:00 PM to 12:55 PM
	LUNCH BREAK							LUNCH BREAK	
	Slot-5	D	D			X	Y	Slot-5	2.00 PM to 2:55 PM
	Slot-6		E					Slot-6	3.00 PM to 3:55 PM
Slot-7							Slot-7	4.00 PM to 4:55 PM	

	Course code	Course Name	L-T-P	TOTAL CONTACT HOURS	FACULTY
A	EC3201	Micro-Controllers and Interfacing	3-0-0	3	Mr.A.Rajkumar(C4,C5) Ms.R.Padmasree (C1,C2), .Ch.Ramya(C3)
B	EC3801	Digital System Design	3-0-0	3	Mr.A.Dheeraj(C1,C2,C3),Mr.A.Sainath Chaithanya(C4,C5)
C	EC3401	Pattern Recognition	3-0-0	3	Mr.G.Sai Radha Krishna(C3,C4,C5), Mr.S. P.Pranaya(C1,C2)
D	CS3203	Operating Systems	3-0-0	3	Mr.S Naveen(C1,C2,C3,C4,C5)
E	BM3203	Operations Research	3-0-0	3	Guest Faculty(C1,C2,C3,C4,C5)
F	EC3902	Mini Project	0-0-4	4	
G	EC3000	Comprehensive Viva-I			
X	EC3801	Micro-Controllers lab	0-0-2	2	Mr.A.Sainath Chaithanya(C3(A),C4(A),C5(A)), Ms.R.Padmasree(C1(A),C2(A),C3(B)) Mr.Md.Arif Ali(C1(B),C2(B),C4(B),C5(B))
Y	EC3802	Digital Systems Design and VLSI Lab	0-0-2	2	Mr.A.Dheeraj(C1(B),C2(B),C3(B))Mr.Ch.Shravan(C1(A),C4(B),C5(B)), Mr.R.Saidulu(C2(A)C3(A),C4(A),C5(A))

( Mr.R.Saidulu)

Faculty I/C, Dept. Timetables

(Mr.B.Bhavsingh)

Faculty I/C, Overall Timetables

(Mr.K.Karthik)

HoD/ECE

(Mrs.G.Srujana)

Associate Dean Engineering