

Rajiv Gandhi University of Knowledge Technologies

(Telangana Government Act 8 of 2016) Basar, Nirmal, Telangana State – 504107, India.

A.Y 22-23_E1_SEM1_ELECTRONICS AND COMMUNICATION ENGINEERING TIME TABLE

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat
	Slot-1			F	D		
	Slot-2	A	X	D			W
C1	Slot-3	Z(T)			A	Z(P)	A
(CSE-124)	Slot-4	В	Е	Е	В		
		LUNCH BREAK					
	Slot-5		D	A		В	
	Slot-6	Y					
	Slot-7						
	Slot-8						

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat
	Slot-1		F		D	A	
	Slot-2	X	В	Е		D	
C2	Slot-3		Z(T)	A	В	В	
(ECE-F25)	Slot-4		A		A	Е	
	LUNCH BREAK						
	Slot-5	D					
	Slot-6					Y	W
	Slot-7				Z(P)		
	Slot-8						

Year	Time	Mon	Tue	Wed	Thu	Fri	Sat
	Slot-1	F	Z(T)		D	D	
C3	Slot-2	Е	D			A	
(ECE-G13)	Slot-3	В	A	В	X	Е	
	Slot-4			A		В	W
	LUNCH BREAK						
	Slot-5	A					
	Slot-6			Y			Z(P)
	Slot-7						
	Slot-8						

		S			
		at			
A	F	A			
D	Е				
	A				
В	Z(T)				
LUNCH BREAK					
		W			
	D B	D E A B Z(T)			

	Time			
Slot-1	09:00 AM to 09:55 AM			
Slot-2	10:00 AM to 10:55 AM			
Slot-3	11:00 AM to 11:55 AM			
Slot-4	12:00 PM to 12:55 PM			
LUNCH BREAK				
Slot-5	2.00 PM to 2:55 PM			
Slot-6	3.00 PM to 3:55 PM			
Slot-7	4.00 PM to 4:55 PM			
Slot-8	5.00PM to 5.55PM			

	Course code	Course Name	L-T-P	TOTAL CONTACT HOURS	FACULTY
А	MA1101	Linear Algebra&Calculus	3-1-0	4	Mr.D.Raju(C1,C2,C3,C4)
В	EC1101	Digital Electronic Circuits	3-0-0	3	Mr.Md.Arif Ali(c1,c2,c3)+Mrs.Ch.Ramya(c4)
X	EC1701	Digital Electronic Circuits Lab	0-0-3	3	Mr.A.Rajkumar(C1-A,C2-A,C3-A,C4- A)+Mrs.Ch.Ramya(c1-B,C2-B,C3-B,c4-B)
	ME1703	Engineering Workshop(Tutorial)	0-1-0	3	Mrs.Anupama (C1, C2, C3, C4)
Z	IVIET/US	Engineering Workshop LAB	0-0-2	5	Mr.Tirupathi(C1, C2)+Mr.Siddaiah(C3,C4)
D	CS1101	Programming for Problem Solving	3-0-0	3	Mrs.U.Nagamani(C1)+Mr.B.Thilak(C3)+ Mr.A.Sreedhar(C2,C4)
Y	CS1701	Programming for Problem Solving Lab	0-0-3	3	Mrs.Latha, Mr.A.Sreedhar (C4)+, Mr.K.Ravikanth, Mr.A.Sreedhar (C2), +Mr.Sujoy Sarkar, Mr.B.Thilak (C3)+Mrs.RajyaLaxmi, Mr.B.Thilak(c1)
Е	BM0005	Constitution of India	2-0-0	2	Ch.Sophia Rani(C1,C2,C3,C4)
W	EC	Element of Electronics and Communications Engineering	0-0-2	2	Mr.V S S K R Naganjaneyulu
F		Counseling Class			Mr. Shiva

(Mr.R.Saidulu)
Faculty I/c, Dept. Timetables

(Mr.B.Bhavsingh)
(Faculty I/c, Overall Timetables)

(Mr.K.Karthik)

(Mrs.G.Srujana)
Associate Dean Engineering